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Summary

· Member of Technical Staff, Bandspeed, Austin (IEEE 802.11abg WiFi chipset design, Verilog, Assembly)
· DSP Engineer, Texas Instruments, Dallas (C, Assembly)
· Circuit Designer, Center for Wireless Communications (Verilog, SPW, FPGA)
· Ph. D. The University of Texas at Austin (Signal Processing, Telecommunications)

Education

The University of Texas at Austin, Austin, TX
Ph.D. in Electrical Engineering, 8/2000 - 05/2004

- Dissertation: *Channel Equalization to Achieve High Bit Rates in Discrete Multitone Modulation Systems*
- Supervisor: Professor Brian L. Evans

National University of Singapore, Republic of Singapore
M.ENG. in Electrical Engineering,12/1999

- Thesis: *Time Domain Equalizer Design For DMT-ADSL Transceivers*
- Supervisor: Professor Behrouz Farhang-Boroujeny

Nankai University, Tianjin, P.R.China
B.S. in Electronics Science, 06/1995

- Major: Electronics and Information System

Working Experience

Member of Technical Staff, Bandspeed, Austin, 06/2004-11/2005
· *Development of BSP1000 802.11a,b,g 3-in-1 3 channel MAC/Baseband Processor for WLAN AP*
  · *Responsible for hardware and firmware implementation of special purpose signal processing units*
  · *Add Interference mitigation and advanced RF monitoring capabilities to Baseband processing*
  · *Assembly and Verilog coding under CVS control*
· *System performance measurement with lab testing equipment*

Texas Instruments DSPS Center, Dallas, 05/2003-08/2003.
· *Development and Implementation of Bit Swap Algorithm on TI's AR7 ADSL2+ modem*
· *C and Assembly Coding on TMSC6201 DSP under Clearcase Control*

Texas Instruments DSPS Center, Dallas, 05/2002-08/2002.

· *DSP Implementation of DMT-FDR structure on TI's AX5 ADSL modem*
· *C and Assembly Coding on TMSC6201 DSP under Clearcase Control*

Texas Instruments DSPS R&D Center, Dallas, 06/2001-08/2001.
· *Investigation of alternative equalization architectures for ADSL*

Development Engineer, Centre for Wireless Communications, National Univ. of Singapore,
04/1999-08/2000.
· *Hardware prototyping of COFDM based digital audio broadcasting receiver,*
Schematic Design in SPW, Verilog HDL Coding, Synthesized for Xilinx Spartan II FPGA
· *Receiver Algorithm Design of Blue-tooth FM Radio*

RF Engineer, National Post & Telecomm. Industrial Co., Shanghai, PRC, 07/1995-07/1997

Patents

· Arthur John Redfern, Nirmal C. Warke and Ming Ding, ``Dual Path Equalization for Multicarrier Systems'', TI-33662, filed by Texas Instruments, Oct 5, 2002, USA.

Teaching Experience

Teaching Assistant, EE306 Introduction to Computing, Fall 2003.

Teaching Assistant, EE345S Real-Time Digital Signal Processing Laboratory, Fall 2000.

Publications

Journal Papers: 6 published, 1 submitted
Conference Papers: 8 published, copies available upon request

Professional Activities

· IEEE Student Member, Since 2000
· Reviewer, IEEE Journal of Selected Areas in Communications
· Reviewer, IEEE Trans on VLSI Systems
· Reviewer, IEEE Trans on Communications
· Reviewer, IEEE Trans on Signal Processing

Honors

· National University of Singapore, Postgraduate Research Scholarship, 07/1997-04/1999
· Nankai University, Motorola Scholarship for academic excellence, 1995
· Nankai University, Third Class Fellowship for academic excellence, 1994
· Nankai University, Guanghua Fellowship for academic excellence, 1993
· Nankai University, First Class Fellowship for academic excellence, 1992

Skills

Simulation Tools: SPW, MATLAB, Agilent Advanced Design System, UC Berkeley Ptolemy
Software Development Tools: TI Code Composer Studio (C6201, C3000), Visual C++
Operating Systems: MS-DOS, UNIX, LINUX
High-Level Languages: C, C++, Java, Fortran, Basic, Verilog HDL
Hardware EDA tools: SUE, Modelsim, Xilinx FPGAEDA kit, Altera Quartus II
Hardware Diagnostic Tools: oscilloscopes, logic analyzer, spectrum analyzer, network analyzer
Hardware Verification: Debussy, Testbuilder